



STGF3NC120HD STGP3NC120HD

7 A, 1200 V very fast IGBT

Features

- Low on-voltage drop ($V_{CE(sat)}$)
- High current capability
- Off losses include tail current
- High speed

Application

- Home appliance
- Lighting

Description

This IGBT utilizes the advanced PowerMESH™ process resulting in an excellent trade-off between switching performance and low on-state behavior.

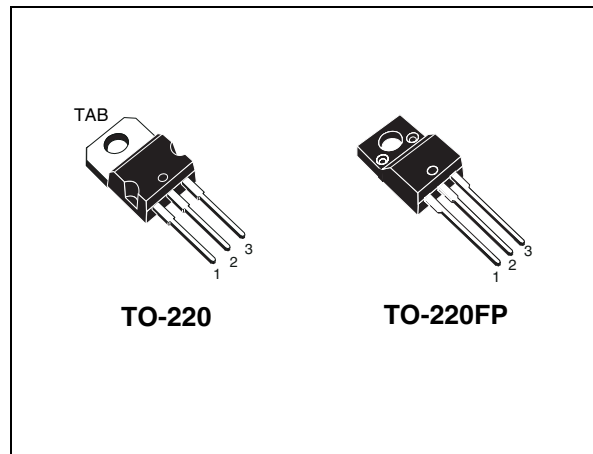


Figure 1. Internal schematic diagram

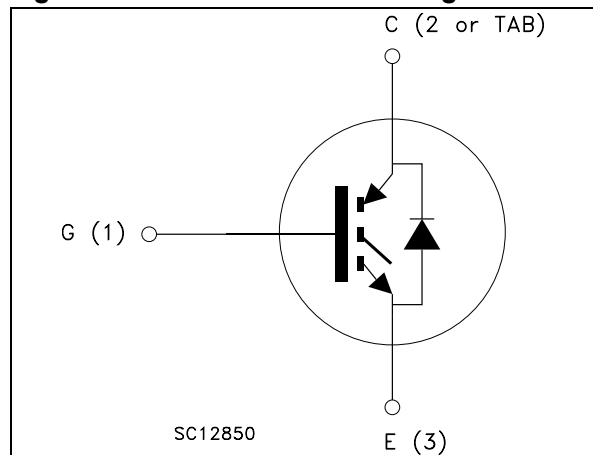


Table 1. Device summary

Order codes	Marking	Packages	Packaging
STGF3NC120HD	GF3NC120HD	TO-220FP	Tube
STGP3NC120HD	GP3NC120HD	TO-220	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-220FP	TO-220	
V_{CES}	Collector-emitter voltage ($V_{GE} = 0$)	1200		V
$I_C^{(1)}$	Continuous collector current at $T_C = 25\text{ °C}$	6	14	A
$I_C^{(1)}$	Continuous collector current at $T_C = 100\text{ °C}$	3	7	A
$I_{CL}^{(2)}$	Turn-off latching current	14		A
$I_{CP}^{(3)}$	Pulsed collector current	20		A
V_{GE}	Gate-emitter voltage	± 20		V
I_F	Diode RMS forward current at $T_C = 25\text{ °C}$	3		A
I_{FSM}	Surge non repetitive forward current $t_p=10\text{ ms}$ sinusoidal	12		A
P_{TOT}	Total dissipation at $T_C = 25\text{ °C}$	25	75	W
V_{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink	2500		V
T_J	Operating junction temperature	-55 to 150		°C

1. Calculated according to the iterative formula:

$$I_C(T_C) = \frac{T_{j(max)} - T_C}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_C(T_C))}$$

2. $V_{clamp} = 80\% V_{CES}$, $T_j = 150\text{ °C}$, $R_G = 10\text{ }\Omega$, $V_{GE} = 15\text{ V}$

3. Pulse width limited by maximum junction temperature and turn-off within RBSOA

Table 3. Thermal data

Symbol	Parameter	Value		Unit
		TO-220FP	TO-220	
$R_{thj-case}$	Thermal resistance junction-case IGBT	5	1.65	°C/W
	Thermal resistance junction-case (diode)	3.5		°C/W
$R_{thj-amb}$	Thermal resistance junction-ambient	62.5		°C/W

2 Electrical characteristics

($T_J = 25\text{ °C}$ unless otherwise specified)

Table 4. Static electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage ($V_{GE} = 0$)	$I_C = 1\text{ mA}$	1200			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}$, $I_C = 3\text{ A}$ $V_{GE} = 15\text{ V}$, $I_C = 3\text{ A}$, $T_J = 125\text{ °C}$		2.3 2.2	2.8	V V
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250\mu\text{A}$	2		5	V
I_{CES}	Collector cut-off current ($V_{GE} = 0$)	$V_{CE} = 1200\text{ V}$ $V_{CE} = 1200\text{ V}$, $T_J = 125\text{ °C}$			50 1	μA mA
I_{GES}	Gate-emitter leakage current ($V_{CE} = 0$)	$V_{GE} = \pm 20\text{ V}$			± 100	nA
$g_{fs}^{(1)}$	Forward transconductance	$V_{CE} = 25\text{ V}$, $I_C = 3\text{ A}$		4		S

1. Pulse duration: 300 μs , duty cycle 1.5%

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies} C_{oes} C_{res}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{CE} = 25\text{ V}$, $f = 1\text{ MHz}$, $V_{GE} = 0$	-	470 45 6	-	pF pF pF
Q_g Q_{ge} Q_{gc}	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 960\text{ V}$, $I_C = 3\text{ A}$, $V_{GE} = 15\text{ V}$	-	24 3 10	-	nC nC nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		15		ns
t_r	Current rise time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$, (see Figure 20)	-	3.5	-	ns
$(di/dt)_{on}$	Turn-on current slope			880		A/ μ s
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		14.5		ns
t_r	Current rise time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	4	-	ns
$(di/dt)_{on}$	Turn-on current slope	$T_J = 125\text{ }^\circ\text{C}$ (see Figure 20)		770		A/ μ s
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		72		ns
$t_{d(off)}$	Turn-off delay time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	118	-	ns
t_f	Current fall time	(see Figure 20)		250		ns
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		132		ns
$t_{d(off)}$	Turn-off delay time	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	210	-	ns
t_f	Current fall time	$T_J = 125\text{ }^\circ\text{C}$ (see Figure 20)		470		ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		236		μ J
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	290	-	μ J
E_{ts}	Total switching losses	(see Figure 20)		526		μ J
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 800\text{ V}$, $I_C = 3\text{ A}$		360		μ J
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\ \Omega$, $V_{GE} = 15\text{ V}$,	-	620	-	μ J
E_{ts}	Total switching losses	$T_J = 125\text{ }^\circ\text{C}$ (see Figure 20)		980		μ J

1. E_{on} is the turn-on losses when a typical diode is used in the test circuit in figure 2. If the IGBT is offered in a package with a co-pack diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25 °C and 125 °C)
2. Turn-off losses include also the tail of the collector current

Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_F	Forward on-voltage	$I_F = 1.5\text{ A}$ $I_F = 1.5\text{ A}$, $T_J = 125\text{ }^\circ\text{C}$	-	1.6 1.3	2.0	V V
t_{rr}	Reverse recovery time	$I_F = 3\text{ A}$, $V_R = 40\text{ V}$,		51		ns
Q_{rr}	Reverse recovery charge	$di/dt = 100\text{ A}/\mu\text{s}$	-	85		nC
I_{rrm}	Reverse recovery current	(see Figure 23)		3.3		A
t_{rr}	Reverse recovery time	$I_F = 3\text{ A}$, $V_R = 40\text{ V}$,		64		ns
Q_{rr}	Reverse recovery charge	$T_J = 125\text{ }^\circ\text{C}$,	-	133		nC
I_{rrm}	Reverse recovery current	$di/dt = 100\text{ A}/\mu\text{s}$ (see Figure 23)		4.2		A

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

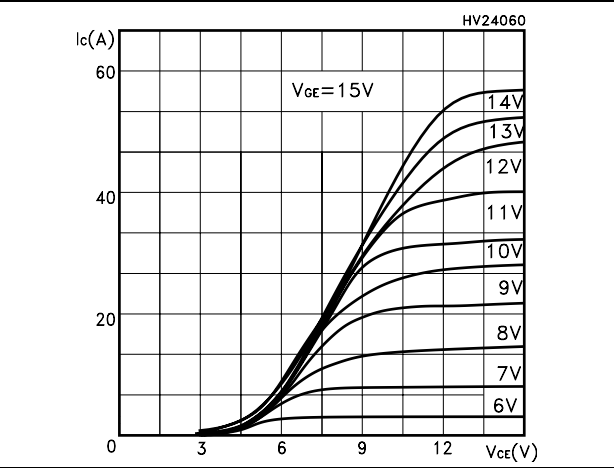


Figure 3. Transfer characteristics

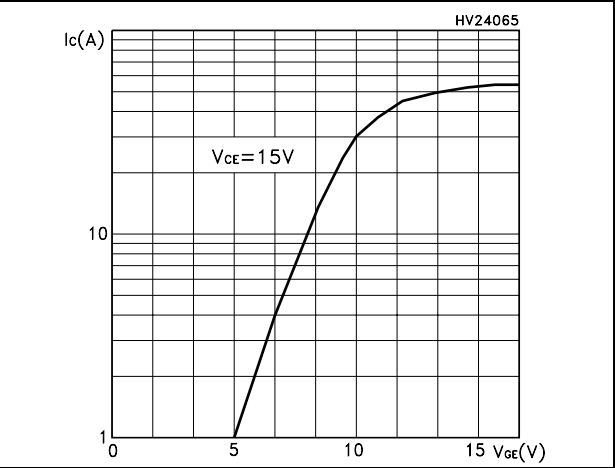


Figure 4. Transconductance

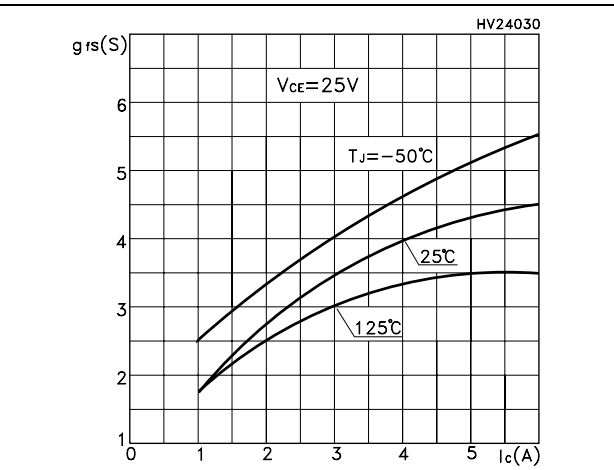


Figure 5. Collector-emitter on voltage vs. temperature

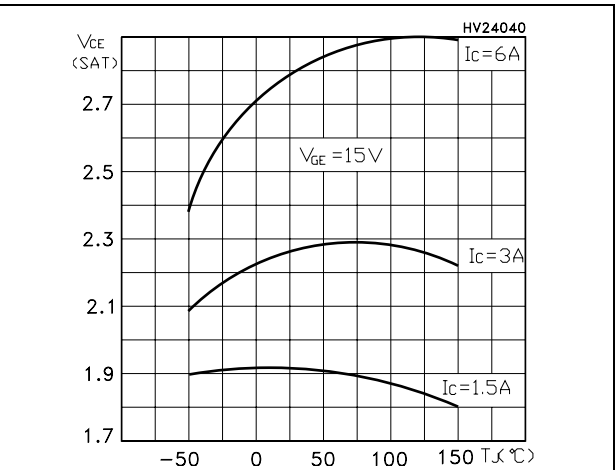


Figure 6. Collector-emitter on voltage vs. collector current

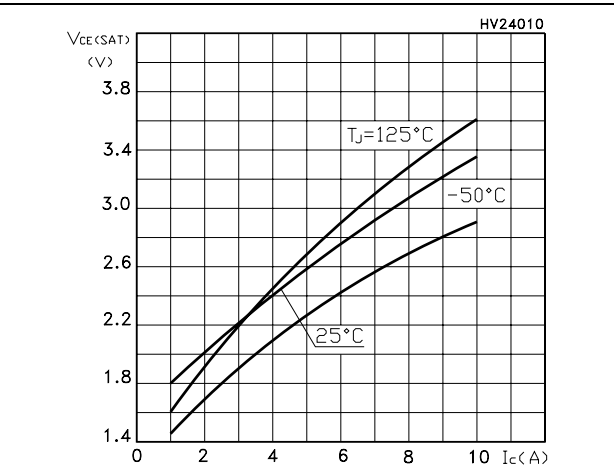


Figure 7. Normalized gate threshold voltage vs. temperature

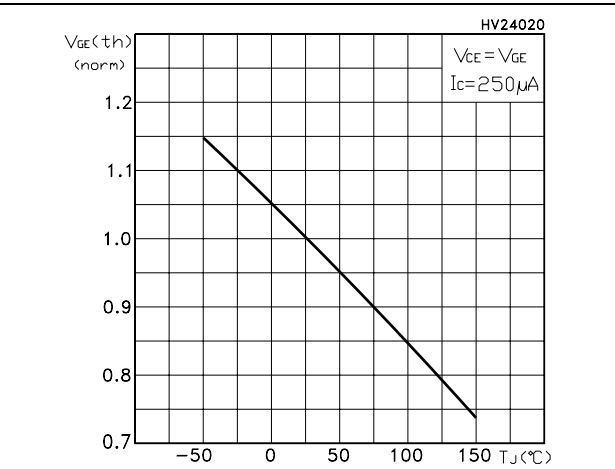


Figure 8. Normalized breakdown voltage vs. temperature

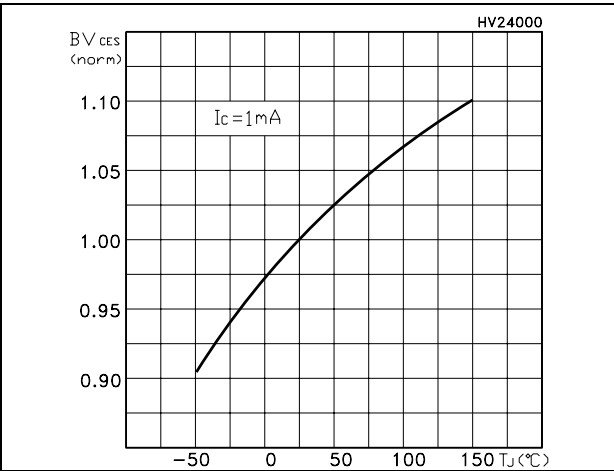


Figure 9. Gate charge vs. gate-source voltage

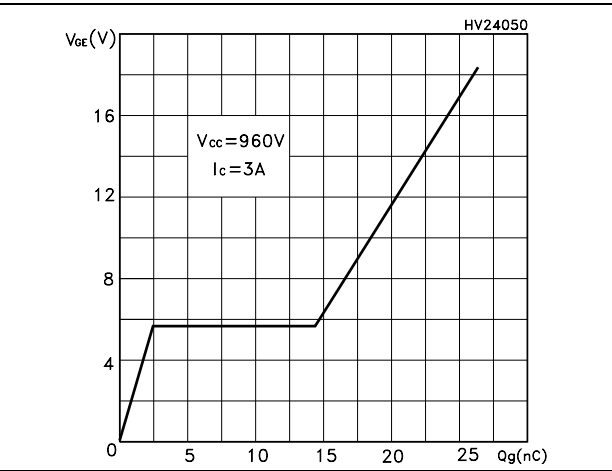


Figure 10. Capacitance variations

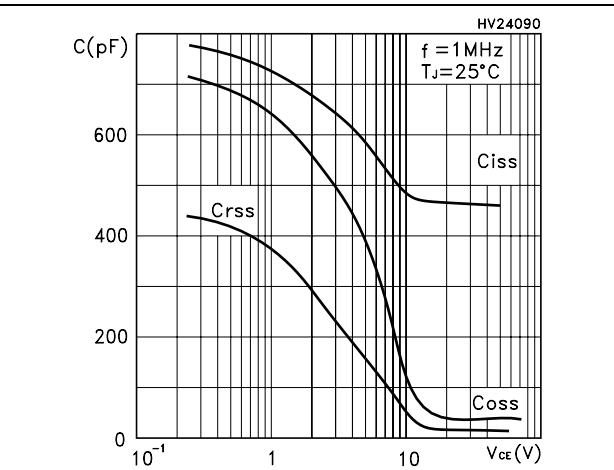


Figure 11. Switching losses vs. temperature

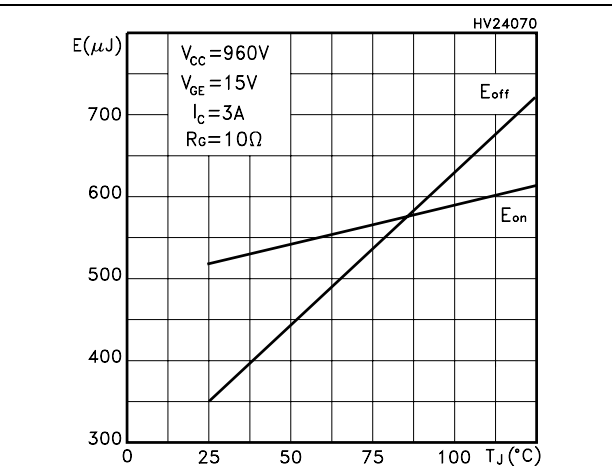


Figure 12. Switching losses vs. gate resistance

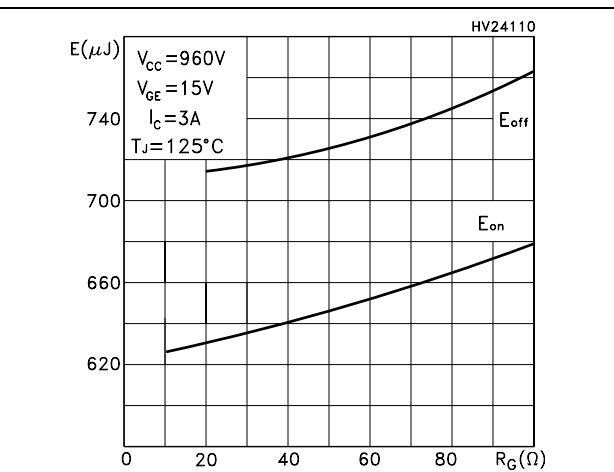


Figure 13. Switching losses vs. collector current

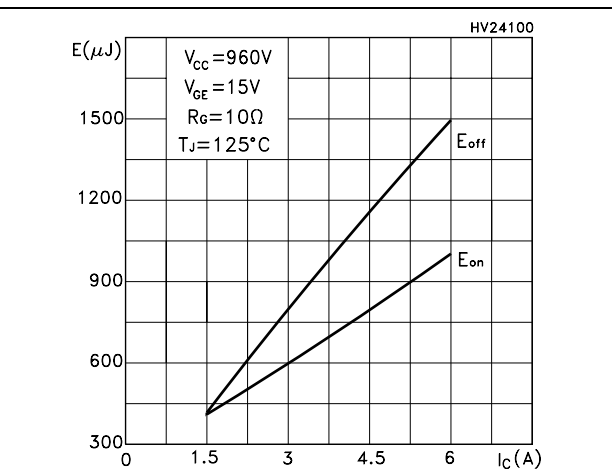


Figure 14. Collector-emitter diode characteristics

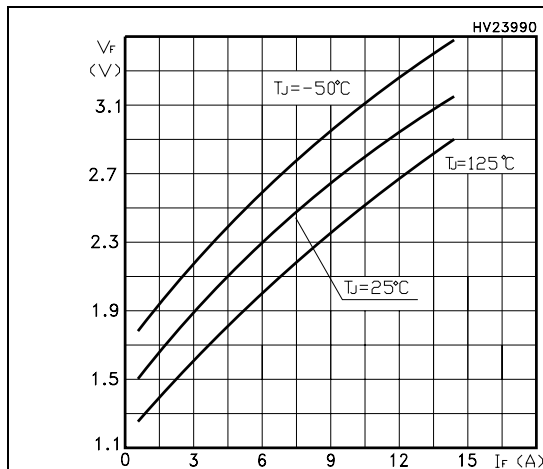
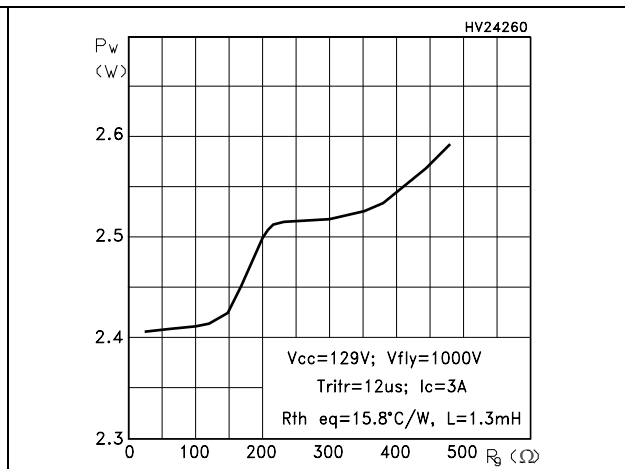
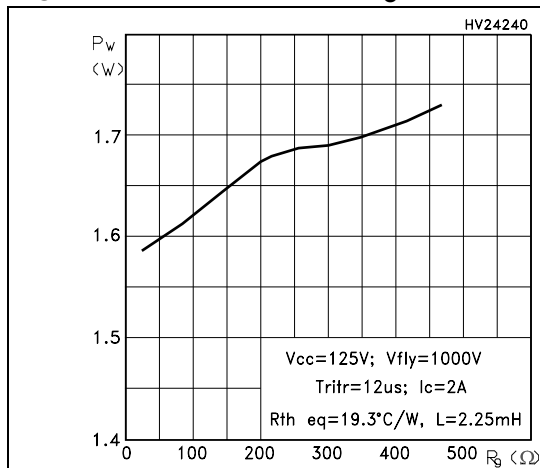
Figure 15. Power losses @ $I_C = 3$ AFigure 16. Power losses @ $I_C = 2$ A

Figure 17. Thermal impedance for TO-220

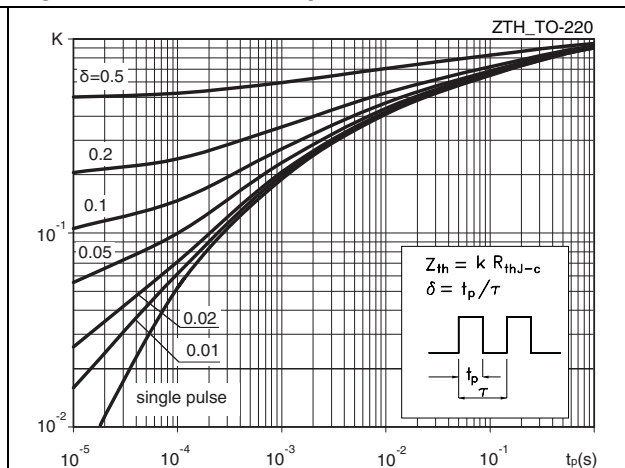


Figure 18. Turn-off SOA

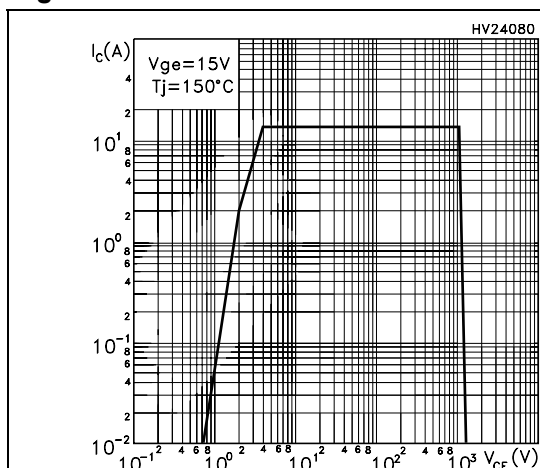
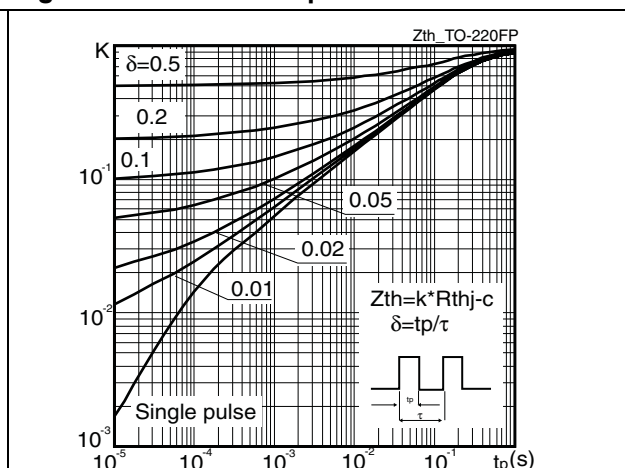


Figure 19. Thermal impedance for TO-220FP



3 Test circuit

Figure 20. Test circuit for inductive load switching

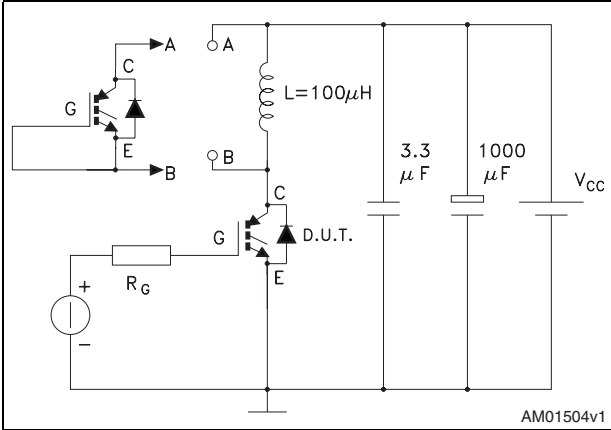


Figure 21. Gate charge test circuit

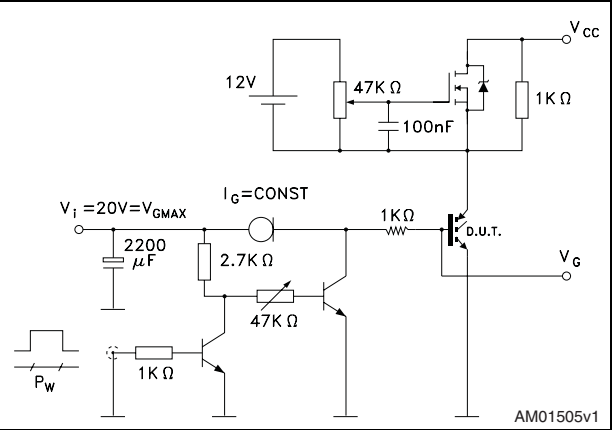


Figure 22. Switching waveform

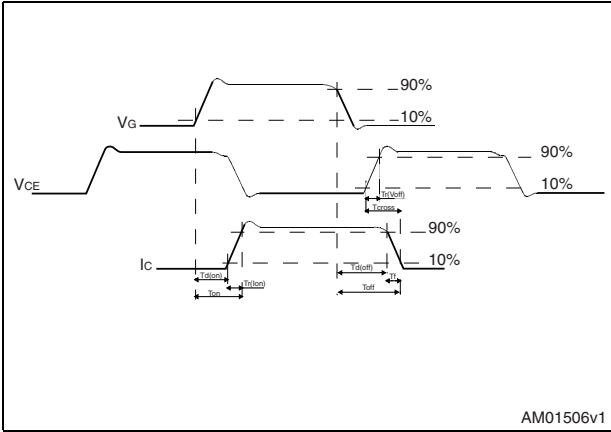
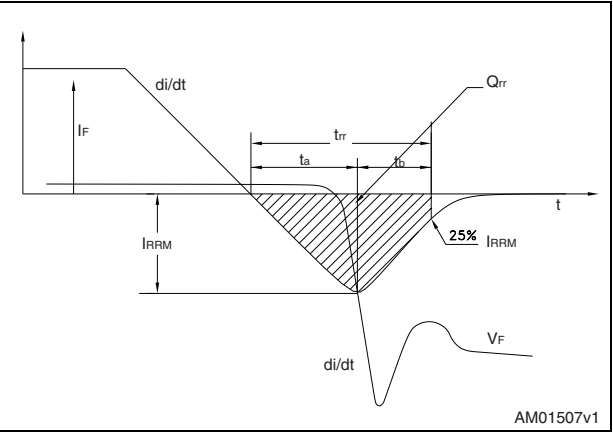


Figure 23. Diode recovery time waveform



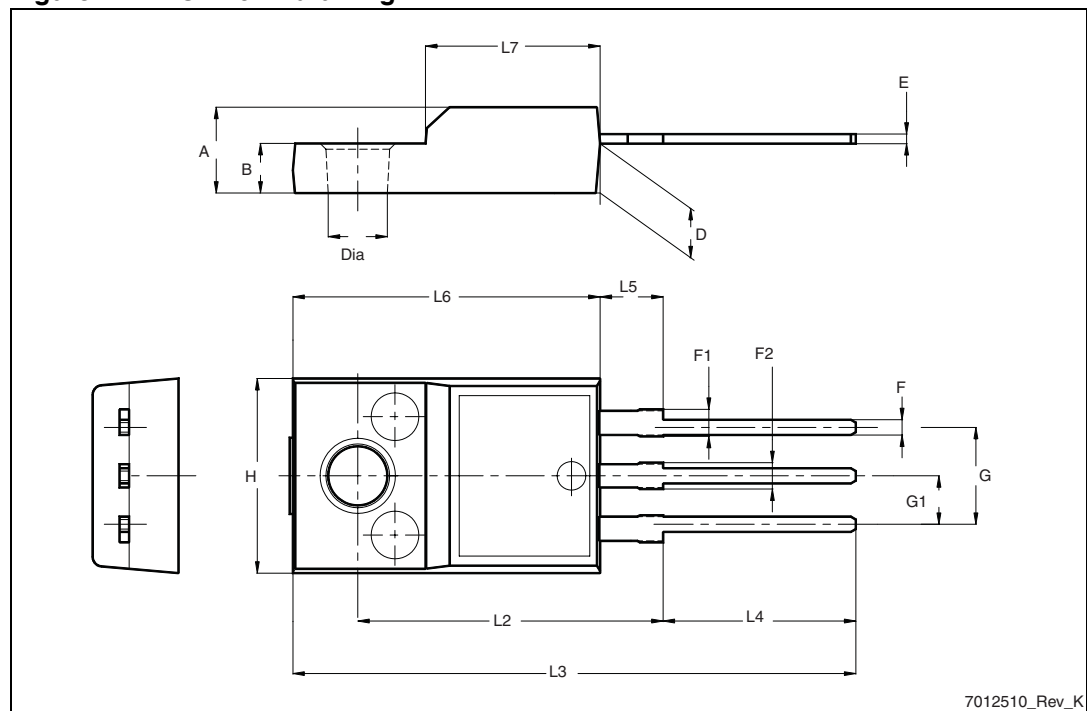
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 9. TO-220FP mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

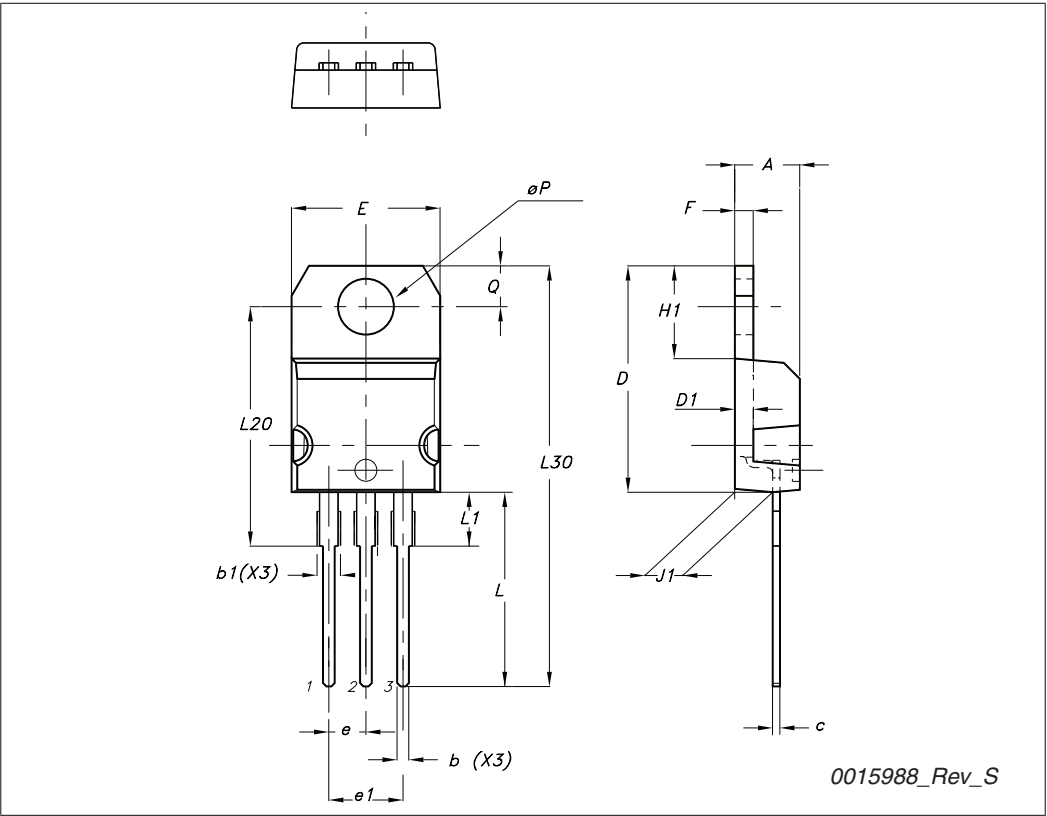
Figure 24. TO-220FP drawing



7012510_Rev_K

TO-220 type A mechanical data

Dim	mm		
	Min	Typ	Max
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95



5 Revision history

Table 10. Document revision history

Date	Revision	Changes
13-Dec-2004	1	First release.
21-Jan-2005	2	Modified Figure 18: Turn-off SOA .
03-May-2010	3	Added new package, mechanical data: TO-220.

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